

### **In the Claims**

This listing of claims will replace all prior versions, and listings, of claims.

### **Listing of Claims**

1-20. (canceled)

21. (new) A detectable flat panel display, comprising:

a substrate;

a circuit device, disposed on the substrate;

a planarizing insulating layer, at least covering the circuit device;

a transparent light-emitting element, disposed on the planarizing insulating layer;

and

a metal pattern, disposed in the planarizing insulating layer, capable of reflecting light through the transparent light-emitting element such that the reflected light is detectable by recognition equipment; and

a transparent insulating cover layer, disposed within and surrounded by the transparent light-emitting element, wherein a projection of the transparent insulating cover layer overlaps the metal pattern.

22. (new) The detectable flat panel display as claimed in claim 21, wherein the projection of the transparent insulating cover layer is only dimensioned to cover the metal pattern for avoiding the reflected light from the metal pattern being interfered by an emitting light from the transparent light-emitting element.

23. (new) The detectable flat panel display as claimed in claim 21, wherein the metal pattern in the planarizing insulating layer is between the circuit device and the transparent light-emitting element.

24. (new) The detectable flat panel display as claimed in claim 21, wherein the transparent light-emitting element comprises:

a transparent anode disposed on the planarizing insulating layer ;

an organic light-emitting layer disposed on the transparent anode ; and

a transparent cathode disposed on the organic light-emitting layer;

wherein the transparent insulating cover layer within the transparent light-emitting element is entirely sandwiched between the transparent anode and the organic light-emitting layer.

25. (new) The detectable flat panel display as claimed in claim 24, wherein the planarizing insulating layer comprising a contact window to expose the metal pattern.

26. (new) The detectable flat panel display as claimed in claim 25, wherein the transparent anode is continuously and conformally extended into the contact window for electrically connecting the metal pattern.

27. (new) The detectable flat panel display as claimed in claim 26, further comprising an opening within the contact window, filled with the transparent insulating cover layer.

28. (new) The detectable flat panel display as claimed in claim 21, wherein the circuit device is a thin film transistor.

29. (new) The detectable flat panel display as claimed in claim 21, wherein the transparent light-emitting element is an organic light-emitting diode.

30. (new) A detectable flat panel display, comprising:

a substrate, comprising a thin film transistor (TFT) region and an organic light-emitting diode (OLED) region;

a thin film transistor disposed in the TFT region and an organic light-emitting diode disposed in the organic light-emitting diode (OLED) region, wherein the thin film transistor comprises a metal pattern, capable of reflecting light through the organic light-emitting diode such that the reflected light is detectable by recognition equipment;

a planarizing insulating layer, at least covering the thin film transistor, wherein the planarizing insulating layer comprises a contact window to expose the metal pattern;  
and

a transparent insulating cover layer, disposed within and surrounded by the organic light-emitting diode, wherein a projection of the transparent insulating cover layer overlaps the contact window and the underlying metal pattern.

31. (new) The detectable flat panel display as claimed in claim 30, wherein the organic light-emitting diode comprises:

a transparent anode in the OLED region, continuously and conformally extended into the contact window for electrically connecting the metal pattern;

an organic light-emitting layer, disposed on the transparent anode; and

a transparent cathode, disposed on the organic light-emitting layer;

wherein the transparent insulating cover layer within organic light-emitting diode is entirely sandwiched between the transparent anode and the organic light-emitting layer.

32. (new) The detectable flat panel display as claimed in claim 30, wherein the projection of the transparent insulating cover layer is only dimensioned to cover the contact window and the underlying metal pattern for avoiding the reflected light from the metal pattern being interfered by an emitting light from the organic light-emitting diode.

33. (new) The detectable flat panel display as claimed in claim 32, further comprising an opening within the contact window, filled with the transparent insulating cover layer.

34. (new) The detectable flat panel display as claimed in claim 33, wherein the metal pattern is a portion of a source electrode, a drain electrode, or a gate electrode of the thin film transistor.

35. (new) The detectable flat panel display as claimed in claim 30, wherein the planarizing insulating layer is a photoresist material or a dielectric material.

36. (new) The detectable flat panel display as claimed in claim 35, wherein the planarizing insulating layer is formed by spin coating.

37. (new) A recognition system, comprising:

a detectable flat panel display; and

recognition equipment,

wherein the detectable flat panel display comprises:

a substrate, comprising a thin film transistor (TFT) region and an organic light-emitting diode (OLED) region;

a thin film transistor disposed in the TFT region and an organic light-emitting diode disposed in the organic light-emitting diode (OLED) region,

a planarizing insulating layer, at least covering the thin film transistor;

a metal pattern, disposed in the planarizing insulating layer, capable of reflecting light through the organic light-emitting diode such that the reflected light is detectable by the recognition equipment; and

a transparent insulating cover layer, disposed within and surrounded by the organic light-emitting diode, wherein a projection of the transparent insulating cover layer is only dimensioned to cover the metal pattern for avoiding the reflected light from the metal pattern being interfered by an emitting light from the organic light-emitting diode.

38. (new) The recognition system as claimed in claim 37, wherein the recognition equipment comprises a light source, a receiver, and a signal feedback device, wherein when light from the light source irradiates the detectable flat panel display, the light is reflected by the metal pattern, the receiver receives a signal of the reflected light and transmits the signal to the signal feedback device, and the signal feedback device transmits the signal back to the detectable flat panel display.